



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov



Bib Data Sheet

CONFIRMATION NO. 2382

SERIAL NUMBER 10/796,270	FILING DATE 03/09/2004 RULE	CLASS 365	GROUP ART UNIT 2824	ATTORNEY DOCKET NO. 384848010US
-----------------------------	---------------------------------------	--------------	------------------------	------------------------------------

APPLICANTS

Jianguo Wang, Cupertino, CA;

David Fong, Cupertino, CA;

Jack Zezhong Peng, San Jose, CA; Fei Ye, Cupertino, CA;

Michael David Fliesler, Santa Cruz, CA;

** CONTINUING DATA *****

This application is a CIP of 10/256,483 09/26/2002 PAT 6,671,040

** FOREIGN APPLICATIONS *****

None

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** SMALL ENTITY **

** 05/28/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 17	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	Verified and Acknowledged Examiner's Signature <i>H. H. Nguyen</i>	Initials		

ADDRESS

25096
PERKINS COIE LLP
PATENT-SEA
P.O. BOX 1247
SEATTLE, WA
98111-1247

TITLE

Methods and circuits for testing programmability of a semiconductor memory cell and memory array using a breakdown phenomenon in an ultra-thin dielectric

FILING FEE

RECEIVED
464

FEES: Authority has been given in Paper
No. _____ to charge/credit DEPOSIT ACCOUNT
No. _____ for following:

<input type="checkbox"/> All Fees
<input type="checkbox"/> 1.16 Fees (Filing)
<input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
<input type="checkbox"/> 1.18 Fees (Issue)
<input type="checkbox"/> Other _____
<input type="checkbox"/> Credit